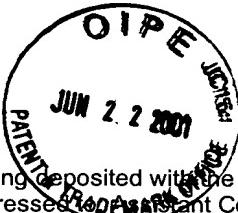
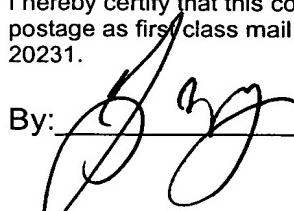


#5

Docket No.: GR 98 P 2191 P



I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

By: 

Date: June 19, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Robert Reiner et al.
Appl. No. : 09/771,886
Filed : January 29, 2001
Title : Clocked Integrated Semiconductor Circuit and Method for
Operating Such a Circuit

CLAIM FOR PRIORITY

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Claim is hereby made for a right of priority under Title 35, U.S. Code, Section 119,
based upon the European Patent Application EP 98 1141 99.7 filed July 29, 1998.

A certified copy of the above-mentioned foreign patent application is being submitted
herewith.

Respectfully submitted,



GREGORY L. MAYBACK
REG NO. 40,719

Date: June 19, 2001

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/mjb